

Under the Paperwork Reduction Act of 1996, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/599,106
				Filing Date	09/19/2006
				First Named Inventor	Ji Zhu
				Art Unit	2812
				Examiner Name	Unknown
Sheet	1	of	2	Attorney Docket Number	IB-1997

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		WU, ET AL., "Germanium Nanowire Growth via Simple Vapor Transport," The American Chemical Society, Vol. 12 (No. 3), p. 605-607, (2000).	
		CUI, ET AL., "Diameter-Controlled Synthesis of Single-Crystal Silicon Nanowires," Applied Physics Letters, Vol. 78 (No. 15), p. 2214-2216, (April 9, 2001).	
		CUI, ET AL., "Doping and Electrical Transport in Silicon Nanowires," The Journal of Physical Chemistry B, Vol. 104 (No. 22), p. 5213-5216, (June 8, 2000).	
		WHANG, ET AL., "Large-Scale Hierarchical Organization of Nanowire Arrays for Integrated Nanosystems," Nano Letters, Vol. 3 (No. 9), p. 1255-1259, (2003).	
		HUANG, ET AL., "Directed Assembly of One-Dimensional Nanostructures into Functional Networks," Science Magazine, Vol. 291, p. 630-633, (January 26, 2001).	
		CUI, ET AL., "Functional Nanoscale Electronic Devices Assembled using Silicon Nanowire Building Blocks," Science Magazine, Vol. 291, p. 851-853, (February 2, 2001).	
		MELOSH, ET AL., "Ultrahigh-Density Nanowire Lattices and Circuits," Science Magazine, Vol. 300, p. 112-115, (April 4, 2003).	
		CHOI, ET AL., "Nanoscale CMOS Spacer FinFET for the Terabit Era," IEEE Electron Device Letters, Vol. 23 (No. 1), p. 25-27, (January 2002).	
		CHOI, ET AL., "Spacer FinFET: Nanoscale Double-Gate CMOS Technology for the Terabit Era," Solid-State Electronics, Vol. 46, p. 1596-1601, (2002).	
		CUI, ET AL., "Nanowire Nanosensors for Highly Sensitive and Selective Detection of Biological and Chemical Species," Science, Vol. 293, p. 1289-1292, (August 17, 2001).	

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /A.O./

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/599,106
				Filing Date	09/19/2006
				First Named Inventor	Ji Zhu
				Art Unit	2812
				Examiner Name	Unknown
Sheet	2	of	2	Attorney Docket Number	IB-1997

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		HAHM, ET AL., "Direct Ultrasensitive Electrical Detection of DNA and DNA Sequence Variations using Nanowire Nanosensors," Nano Letters, Vol. 4 (No. 1), p. 51-54, (2004).	
		DUAN, ET AL., "Indium Phosphide Nanowires as Building Blocks for Nanoscale Electronic and Optoelectronic Devices," Nature, Vol. 409, p. 66-69, (January 4, 2001).	

Examiner Signature	/Allan Olsen/	Date Considered	12/15/2010
--------------------	---------------	-----------------	------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /A.O./